

HEP activities at the "VLSI design lab." in the UB

Monday, 9 July 2018 12:45 (25 minutes)

I will present the activities that our group have performed in the HEP field during the last years. Our expertise is in VLSI design, therefore, I will present several ASIC designs devoted for different detectors.

Presenter: Dr ALONSO, Oscar (Univ. of Barcelona)