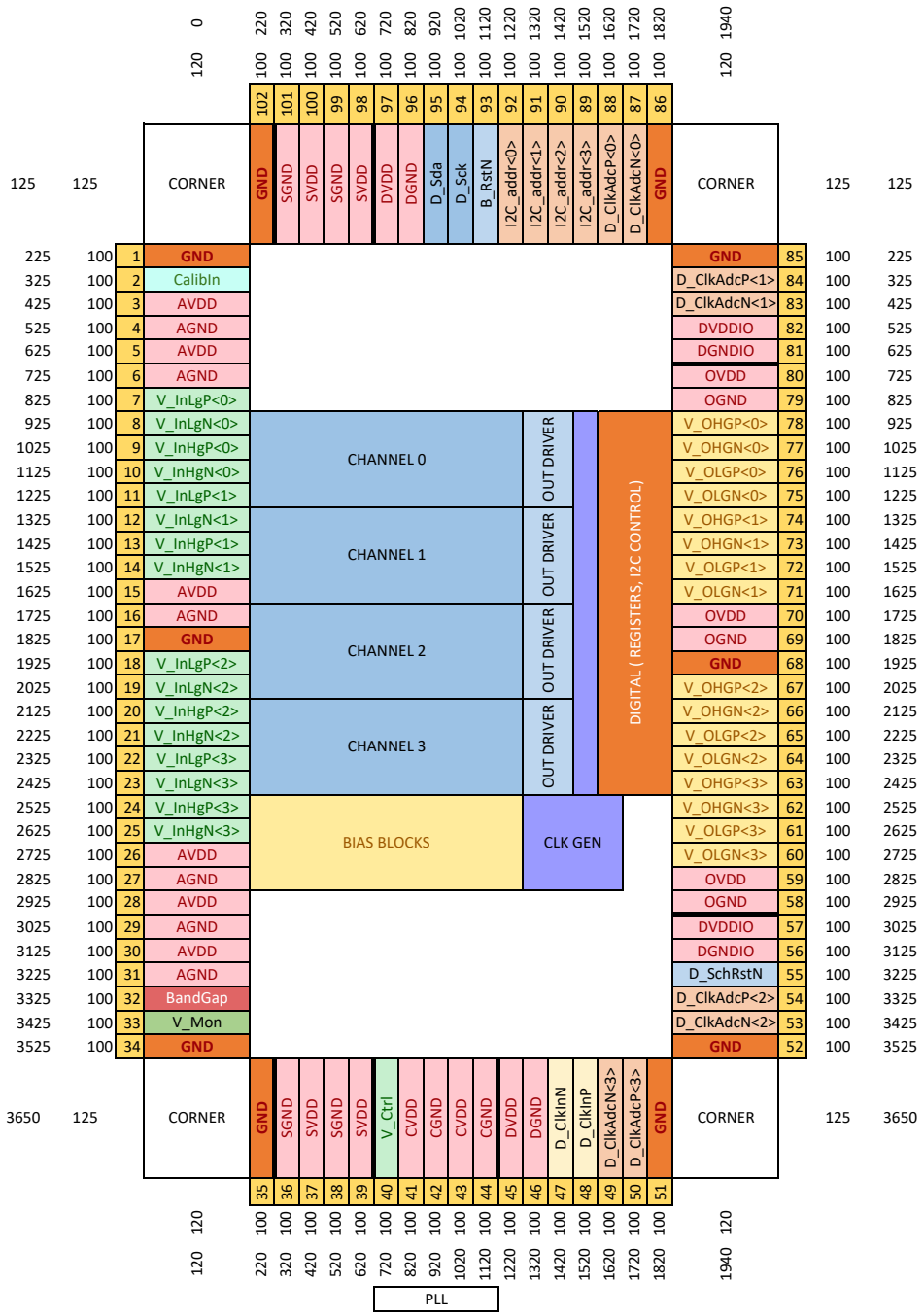

Pad Count:

V_In*	16
V_O*	16
D_ClkIn*	2
D_ClkAdc*	8
I2C_addr*	4
D_S*	2
Rst	2
V_ctrl	1
V_Mon	1
CalibIn	1
BandGap	1
AVDD	6
AGND	6
AVDDIO	0
AGNDIO	0
SVDD	4
SGND	4
SVDDIO	0
SGNDIO	0
CVDD	2
CGND	2
CVDDIO	0
CGNDIO	0
DVDD	2
DGND	2
DVDDIO	2
DGNDIO	2
OVDDIO	0
OGNDIO	0
OVDD	3
OGND	3
GND	10
TOTAL	102

Notes:

El Pad digital que aniria per g

DGND=GND nomes pel digita
PADRING Analogic es passiu,

SLVS Transmitter no es digita
SLVS Digital, integrar el trans
CLK IN es digital, el receptor

D_ClkAdcN<3> estos enviarlc

CERN_v22 es la que usamos
Coger CERN_SLVS, copiada